

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	IS&R	L1	2750	((385/88) or (385/89) or (385/92) or (385/93) or (385/94)).CCLS.	USPAT; US-PGPUB; EPO; JPO	2003/11/25 16:50			0
2	BRS	L2	63066	(edge or edges or periphery) with (card or cards or board or boards or motherboard or motherboards or daughterboard or daughterboards)	USPAT; US-PGPUB; EPO; JPO	2003/11/25 16:50			0
3	BRS	L3	186	1 and 2	USPAT; US-PGPUB; EPO; JPO	2003/11/25 16:50			0
4	BRS	L4	5329	(slot or slots or rail or rails) with (guide or guides or guiding) with (card or cards or board or boards or motherboard or motherboards or daughterboard or daughterboards)	USPAT; US-PGPUB; EPO; JPO	2003/11/25 16:50			0
5	BRS	L5	21	1 and 4	USPAT; US-PGPUB; EPO; JPO	2003/11/25 16:51			0
6	BRS	L6	29	(3 or 5) and @pd>20030419	USPAT; US-PGPUB; EPO; JPO	2003/11/25 16:53			0